

**WEST**[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Terms	Documents
L17	0

US Patents Full-Text Database  
US Pre-Grant Publication Full-Text Database  
JPO Abstracts Database  
EPO Abstracts Database  
Derwent World Patents Index

**Database:** IBM Technical Disclosure Bulletins

**Search:**

L18

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**Search History**

**DATE:** Tuesday, October 15, 2002   [Printable Copy](#)   [Create Case](#)

**Set Name Query**  
side by side

**Hit Count Set Name**  
result set

*DB=TDBD; PLUR=YES; OP=OR*

L18 L17

0 L18

*DB=DWPI; PLUR=YES; OP=OR*

L17 L16

0 L17

*DB=EPAB; PLUR=YES; OP=OR*

L16 L15

0 L16

*DB=JPAB; PLUR=YES; OP=OR*

L15 L14

0 L15

*DB=PGPB; PLUR=YES; OP=OR*

L14 L13

0 L14

*DB=USPT; PLUR=YES; OP=OR*

L13 L12

0 L13

*DB=TDBD; PLUR=YES; OP=OR*

L12 L11 and "common source"

0 L12

L11 L10 and ("plural drains" OR "plural drain regions" OR "drain regions")

9 L11

L10 "floating gates"

80 L10

L9 ("Floating gate" near8 drain) and ("floating gate" near7 source) and "common source" and l1

0 L9

L8 L7

0 L8

*DB=DWPI; PLUR=YES; OP=OR*

L7 L6

0 L7

*DB=EPAB; PLUR=YES; OP=OR*

L6 L5

0 L6

*DB=JPAB; PLUR=YES; OP=OR*

L5 L4

0 L5

*DB=PGPB; PLUR=YES; OP=OR*

L4 L3

0 L4

*DB=USPT; PLUR=YES; OP=OR*

L3 ("Floating gate" near4 drain) and ("floating gate" near4 source) and "common source" and l1

0 L3

L2 L1 and "floating gates"

3546 L2

L1 "flash memory"

12773 L1

END OF SEARCH HISTORY